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NO. 0293 P.

#16/D
PATENT

83256

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application

Applicant: Seigfried Hart et al.

Serial No.: 09/889,260

Filed: January 3, 2003

For: **LOWER JITTER ANALOG-
DIGITAL LOCKED LOOP WITH
LOCK DETECTION CIRCUIT**
(As Amended)

Examiner: Minh T. Nguyen

) Art Unit: 2816

) Confirmation No.: 1457

) I heraby certify that this correspondence is
) being transmitted via facsimile to 703-746-8825
) to: MAIL STOP AMENDMENT - FEE,
) Commissioner for Patents, P.O. Box 1450,
) Alexandria, Virginia 22313-1450, on November
) 10, 2003

) Gerald T. Shekleton
) Gerald T. Shekleton Reg. No. 27,466 Date

SUBSTITUTE AMENDMENT

MAIL STOP AMENDMENT - FEE

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

Sir:

The Office Action of May 27, 2003 has been carefully reviewed and the following amendments and remarks are made in response thereto.

A Petition for Extension of Time Under 37 CFR 1.136(a) is enclosed.

12/3/03
Shm H

Serial No. 09/889,260

IN THE TITLE:

Please change the title to "LOW JITTER ANALOG-DIGITAL LOCKED LOOP WITH LOCK DETECTION CIRCUIT".

IN THE ABSTRACT:

Please substitute the enclosed page for the abstract currently on file.

IN THE SPECIFICATION:

Please delete the paragraph on page 3, lines 20-23. ✓